

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Chih-Ching Shih et al.
Application No. : Not yet available
Confirmation No. : Not yet available
Filed : Concurrently herewith
For : ELECTRICALLY-PROGRAMMABLE TRANSISTOR
ANTIFUSES
Group Art Unit :
Examiner :

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97, and 1.98, applicants hereby bring the attention of the Examiner to the documents listed on the attached Form PTO-1449 (submitted in duplicate).

A notice in the Official Gazette of August 5, 2003 waived the requirement under 37 C.F.R. § 1.98(a)(2)(i) for submitting a copy of each U.S. patent in Information Disclosure Statements for patent applications filed after June 30, 2003. Accordingly, no copies of the cited U.S. patents are enclosed. A

copy of each listed document that is not a U.S. patent is
enclosed herewith.

Respectfully Submitted,

 2/16/04

G. Victor Trey
Reg. No. 36,294
Attorney for Applicants
Customer No. 36532

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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Complete if Known	
		Application Number	
		Filing Date	
		First Named Inventor	Chih-Ching Shih
		Art Unit	
		Examiner Name	
Sheet 1 of 2	Attorney Docket Number		A1351

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US- 4,207,556	06-10-1980	Yoshi et al.	
		US- 4,433,331	02-21-1984	Kollaritsch	
		US- 4,609,986	02-21-1986	Hartmann et al.	
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		US- 4,761,768	08-02-1988	Turner et al.	
		US- 4,642,487	02-10-1987	Carter	
		US- 5,510,730	04-23-1996	El Gamal et al.	
		US- 5,291,434	03-01-1994	Kowalski	
		US- 6,477,094	11-05-2002	Kim et al.	
		US- 6,456,546	09-24-2002	Kim et al.	
		US- 5,303,199	04-12-1994	Ishihara et al.	
		US- 5,070,384	12-03-1991	McCollum et al.	
		US- 5,463,244	10-31-1995	De Araujo et al.	
		US- 6,125,069	09-26-2000	Aoki	
		US- 6,108,261	08-22-2000	Kim et al.	
		US- 5,844,298	12-01-1998	Smith et al.	
		US- 5,831,923	11-03-1998	Casper	
		US- 10/646,013	N/A	Cheng H. Huang et al.	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ Number ⁴ Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶

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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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		Filing Date	
		First Named Inventor	Chih-Ching Shih
		Art Unit	
		Examiner Name	
Sheet	2	of	2
		Attorney Docket Number	A1351

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		WEI ZHANG ET AL., "Energy Effect of the Laser-Induced Vertical Metallic Link", IEEE Transactions on Semiconductor Manufacturing, Vol 14, No. 2, May 2001 pp 163-169	
		"Analyzing the process window for laser copper-link processing" Solid State Technology - Semiconductor manufacturing and wafer fabrication, Jan 8, 2003	
		ALEXANDER KALNITSKY et al. "CoSi2 integrated fuses on poly silicon for low voltage 0.18 um CMOS applications" (c) 1999 IEEE	
		MOHSEN ALAVI "A PROM Element Based on Salicide Agglomeration of Poly Fuses in a CMOS logic process" IEEE International Electron Devices Meeting, December 1997	
		NORIAKI SATO et al. "A New Programmable Cell Utilizing Insulator Breakdown", IEDM 1985, pp 639-642	
		JINBONG KIM et al. "Three-Transistor One-Time Programmable (OTP) ROM Cell Array Using Standard CMOS Gate Oxide Antifuse", IEEE Electron Device Letters, Vol 24, No. 9, September 2003 pp. 589-591	
		WEI ZHANG et al. "Laser-Formed Vertical Metallic Link and Potential Implementation in Digital Logic Integration", PROC MAPLD, MD, 1999 pp 1-7 (B5)	

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